

04-21-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richard K. Williams and Wayne Grabowski
Assignee: Advanced Analogic Technologies, Inc.
Title: Self-aligned Trench Transistor Using Etched Contact
Serial No.: 10/767,028 Filing Date: January 28, 2004
Examiner: Not assigned yet Group Art Unit: Not assigned yet
Docket No.: M7207US5D

San Jose, California
April 19, 2004

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313

INFORMATION DISCLOSURE STATEMENT UNDER CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants call to the attention of the Examiner the documents listed on the attached PTO Form SB08A.

Copies of the listed documents are enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

No fee is believed required for submission of this information disclosure statement.

However, the Commissioner is hereby authorized to charge any fees that may be required for

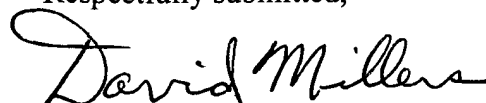


consideration of this information disclosure statement to Deposit Account No. 50-1767.

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Respectfully submitted,



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